Amendments to the Specification:

Please delete the heading "FIELD OF THE INVENTION" on page 1, line 3, and the following paragraph beginning on page 1, line 4.

Please delete the heading "BACKGROUND OF THE INVENTION " on page 1, line 8, and the following paragraph beginning on page 1, line 9.

Please amend the paragraph beginning on page 4, line 3 as follows:

A computer manufacturing system is disclosed. The computer manufacturing system comprises a system under test (SUT), the SUT including a network adapter and a boot loader for loading the appropriate operating system. The SUT further includes a station for receiving customer orders for the SUT, the station including a sequencer. The sequencer obtains a boot selection file for the SUT from a directory. The SUT further retrieves and parses the boot selection file to obtain the operating system image to load and boot until the SUT is configured with the appropriate ~~date~~ data.

Please amend the paragraph beginning on page 5, line 3 as follows:

Figure 4 is a diagram of a ~~STU~~ SUT in accordance with the present invention.

Please amend the paragraph beginning on page 6, line 14 as follows:

To describe the features of the present invention in more detail, refer now to the following description along with the accompanying figures. Figure 2 is a block diagram of an architecture 100 for manufacturing a computer system in accordance with the present invention. The architecture includes a level 2 server 112. The level 2 server 112 functions as an image repository

and a legacy code repository. The level 2 server 112 includes an SUT specific ~~directly~~ directory that stores the product process definition for the SUTs associated with the manufacturing process. The level 2 server 112 passes customer orders to the stations 200. The level 2 server 112 also launches code which performs the initial binding. The level 2 server 112 is coupled to a first Ethernet switch 113. The first Ethernet switch 113 provides access to a plurality of level 1 servers 114. The level 1 server 114 includes a plurality of servers such as a Windows-based server, a PXE Server (DOS only), and a DHCP Server. Each of the level 1 servers 114 ~~contain~~ contains a SUT-specific (MTSN) directory. Each of the level 1 servers 114 is coupled to a second Ethernet switch 116. The second Ethernet switch 116 is coupled to a plurality of stations 200. Each station 200 is coupled to one system under test (SUT) 300.

Please amend the paragraph beginning on page 7, line 10 as follows:

The station 200 performs the in-process binding. The station 200 also launches the sequencer 202 based on the process definition file in the MTSN directory for a given SUT. Commands are executed on the station 200 for controlling the power cycler or for providing service processor communication. The station 200 is preferably LINUX-based and includes a sequencer 202 that allows for identifying of each SUT. Referring back to Figure 2, a power cycler ~~202~~ 222 is coupled to the SUTs and the stations for cycling the SUTs on and off as required based upon commands from the stations.[[.]]

Please amend the paragraph beginning on page 7, line 17 as follows:

Figure 4 is a diagram of a SUT 300 in accordance with the present invention. The SUT 300 includes a network adapter 302 and boot loader 304. The network adapter 302 is standard protocol compatible, such as being compatible with the PXE protocol. The boot loader 304 interacts with

the sequencer 202 of station 200 to provide the appropriate operating system to the SUT in the manufacturing process. Commands are executed locally on the SUT to provide the booting of the SUT for the various manufacturing operations. Accordingly, the station 200 upon receipt of the proper commands from the level 1 server interacts with the SUT 300 to facilitate the manufacturing process. To describe this interaction in more detail, refer to the following discussion in conjunction with the accompanying figures.